

CUSTOMER NO.: 24498  
Serial No.: 10/518,569  
Office Action dated: July 6, 2009  
Response dated: October 5, 2009

PATENT  
PU020298

Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently Amended) A method for extracting selected time information from a stream of serialized AES digital audio data, comprising:

detecting, by a broadcast router, a first transition indicative of a first preamble of said stream of serialized AES digital audio data;

detecting, by the broadcast router, a second transition indicative of a subsequent preamble of said serialized AES digital audio data; [[and]]

determining, by the broadcast router, a time separating said first and second transitions; and

transferring the determined time to a decoding logic circuit for decoding said stream of serialized AES digital audio data by utilizing the determined time.

2. (Cancelled)

3. (Cancelled)

4. (Currently Amended) The method of claim [[3]] 1, wherein said time information is determined in the form of a fast-clock pulse count separating said first and second transitions, wherein said fast-clock pulse count is a count of fast-clock pulses.

5. (Currently Amended) The method of claim [[4]] 1, wherein said first transition and said second transition are separated by thirty-one intervening transitions, wherein said thirty-one intervening transitions are not indicative of said subsequent preamble of said serialized AES digital audio data.

CUSTOMER NO.: 24498  
Serial No.: 10/518,569  
Office Action dated: July 6, 2009  
Response dated: October 5, 2009

PATENT  
PU020298

6. (Currently Amended) The method of claim 1, wherein said determined time information is suitable for use in encoding said stream of serialized AES digital audio data.

7. (Currently Amended) The method of claim 6, and further comprising transferring said determined time information to an encoding logic circuit for use in encoding said stream of serialized AES digital audio data.

8. (Currently Amended) The method of claim 7, wherein said time information is determined in the form of a fast-clock pulse count separating said first and second transitions, wherein said fast-clock pulse count is a count of fast-clock pulses.

9. (Previously Presented) The method of claim 8, wherein said first transition and said second transition are separated by thirty-one intervening transitions, wherein said thirty-one intervening transitions are not indicative of said subsequent preamble of said serialized AES digital audio data.

10. (Currently Amended) A broadcast router comprising:  
a decoder circuit coupled to receive a stream of serialized AES digital audio data, said decoder circuit extracting time information from said stream of serialized AES digital audio data during the decoding thereof wherein said time information is based on determining a time separating a first transition, indicative of a first preamble of said stream of serialized AES digital audio data, and a second transition, indicative of a second preamble of said stream of serialized AES digital audio data and utilizing said extracted time information to decode said received stream of serialized AES digital audio data; and

a target component coupled to said decoder circuit, said target component receiving said extracted time information from said stream of serialized AES digital audio data;

CUSTOMER NO.: 24498  
Serial No.: 10/518,569  
Office Action dated: July 6, 2009  
Response dated: October 5, 2009

PATENT  
PU020298

wherein said target component utilizes said extracted time information while executing at least one function thereof.

11. (Cancelled)
12. (New) A method for extracting selected time information from a stream of serialized AES digital audio data, comprising:
  - detecting, by a broadcast router, a first transition of the stream of serialized AES digital audio data;
  - counting, by a broadcast router, a number of transitions of the serialized AES digital audio data from the first transition until the number of transition reaches a count of 33;
  - counting a number of clock pulses of a clock from the detecting of the first transition of the serialized AES digital audio data until the number of transitions reaches the count of 33; and
  - outputting the clock count to a decoding logic circuit.
13. (New) The method of claim 12, wherein the clock is a higher frequency than a frequency of the transitions of the serialized AES digital audio data.
14. (New) The method of claim 12, wherein the clock is a fast clock.